

Docket No.: 251144US2

COMMISSIONER FOR PATENTS ALEXANDRIA, VIRGINIA 22313

RE: Application Serial No.: 10/809,475

Applicants: Tetsu HASEGAWA, et al.

Filing Date: March 26, 2004

For: A SELF-TEST EXECUTABLE INTEGRATED

CIRCUIT, A DESIGN APPARATUS THEREOF, AND

A SCAN CHAIN DESIGN APPARATUS

Group Art Unit: 2138

Examiner: RADOSEVICH, S.

SIR:

Attached hereto for filing are the following papers:

ELECTION

Our credit card payment form in the amount of \$0.00 is attached covering any required fees. In the event any variance exists between the amount enclosed and the Patent Office charges for filing the above-noted documents, including any fees required under 37 C.F.R 1.136 for any necessary Extension of Time to make the filing of the attached documents timely, please charge or credit the difference to our Deposit Account No. 15-0030. Further, if these papers are not considered timely filed, then a petition is hereby made under 37 C.F.R. 1.136 for the necessary extension of time. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

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DOCKET NO: 251144US2

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF

TETSU HASEGAWA, ET AL. : EXAMINER: RADOSEVICH, S.

SERIAL NO: 10/809,475

FILED: MARCH 26, 2004 : GROUP ART UNIT: 2138

FOR: A SELF-TEST EXECUTABLE INTEGRATED CIRCUIT, A DESIGN APPARATUS THEREOF, AND A SCAN

CHAIN DESIGN APPARATUS

ELECTION

COMMISSIONER FOR PATENTS ALEXANDRIA, VIRGINIA 22313

SIR:

In response to the Restriction Requirement dated June 26, 2006, applicants elect the invention of Group I without traverse, identifying Claims 1-18 as readable on the elected invention.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND, MAIER & NEUSTADT, P.C.

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